



10

13



FIG. 1

FIG. 1

FIG. 2

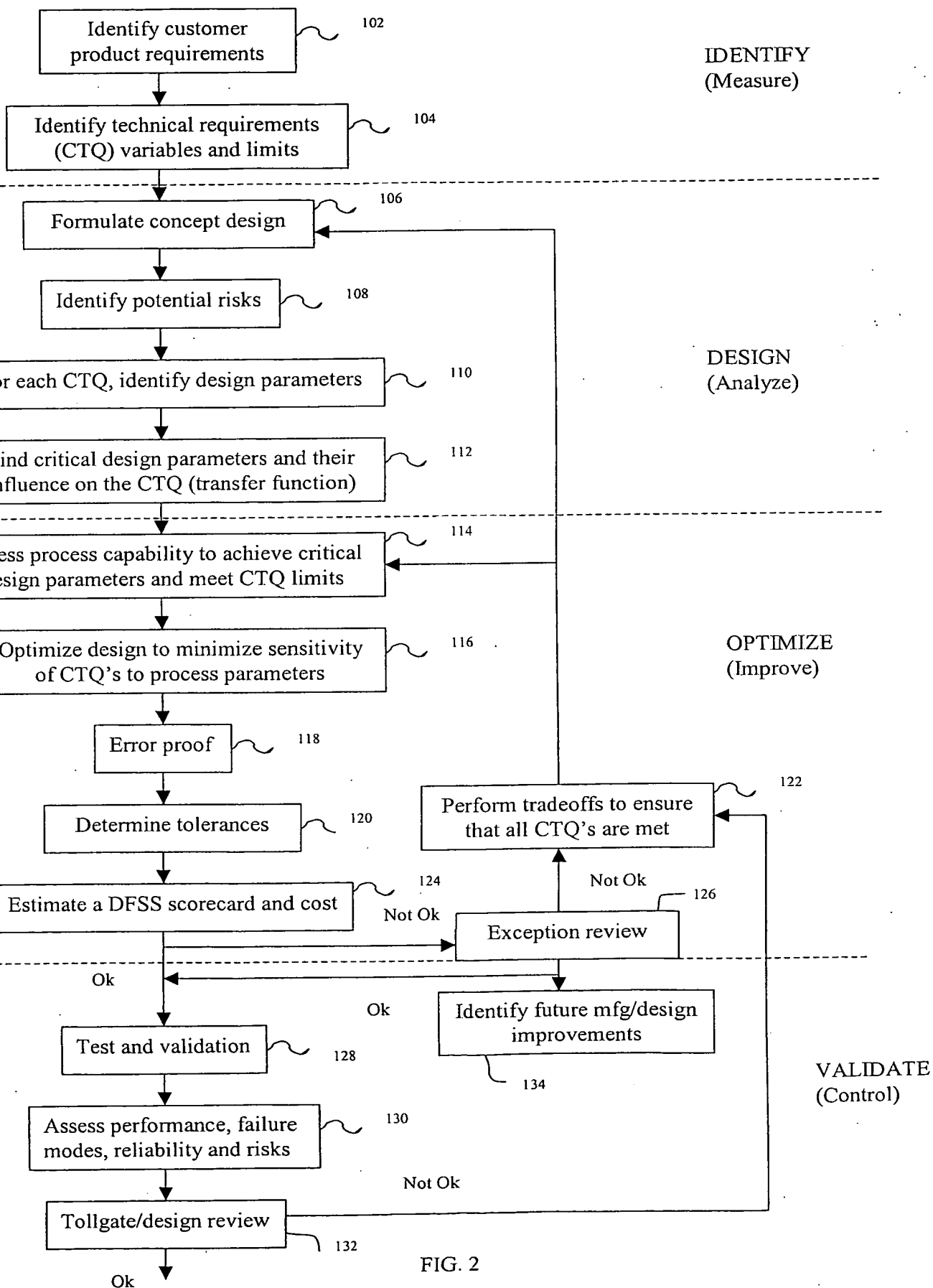


FIG. 2

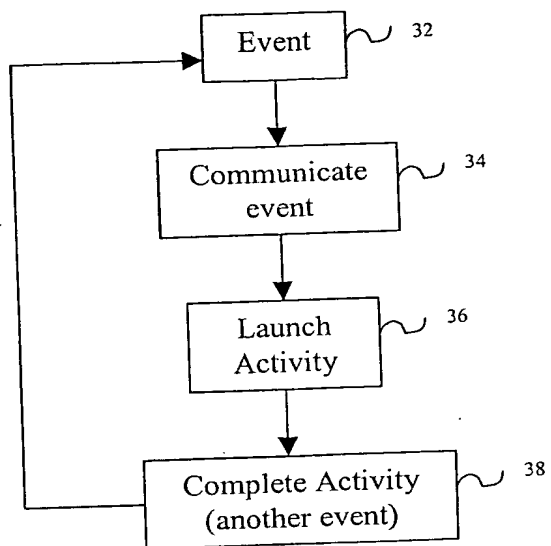
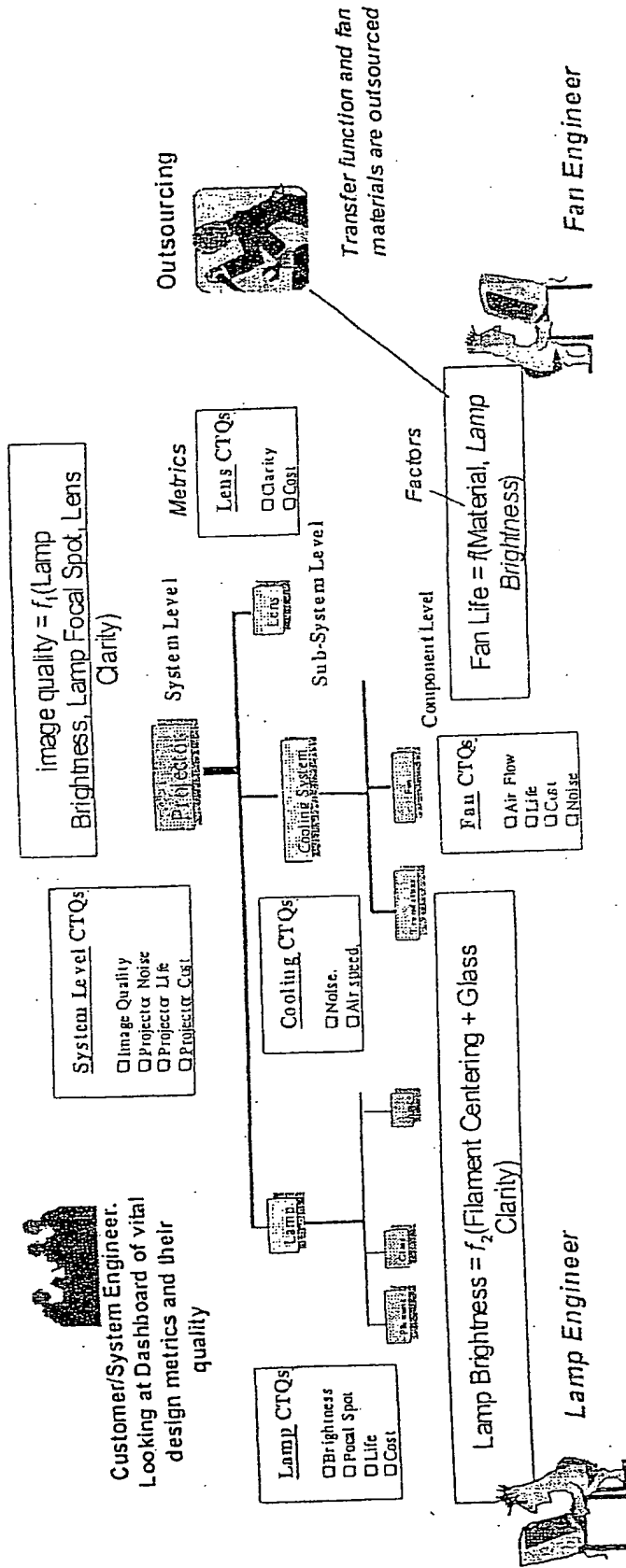


FIG. 3



F/G. 4

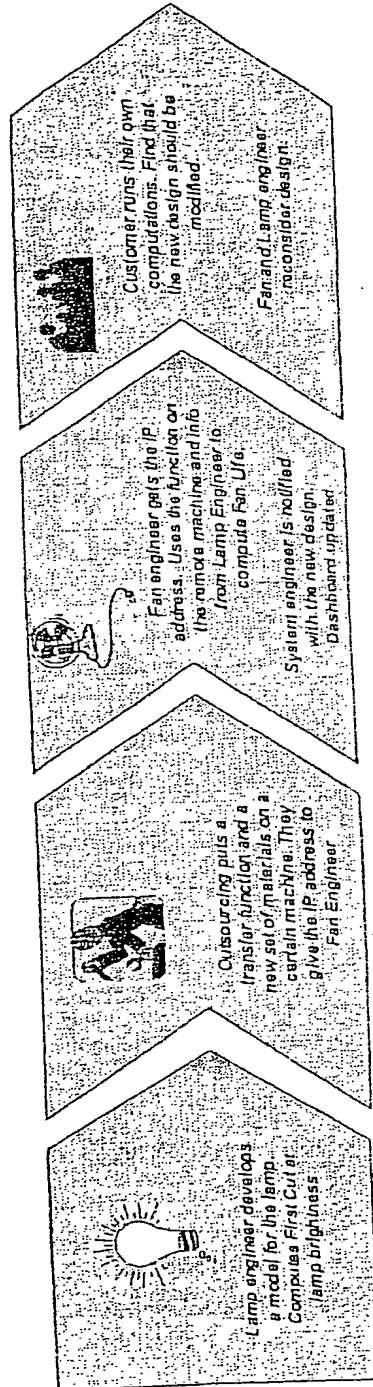


FIG. 5

FIG. 6

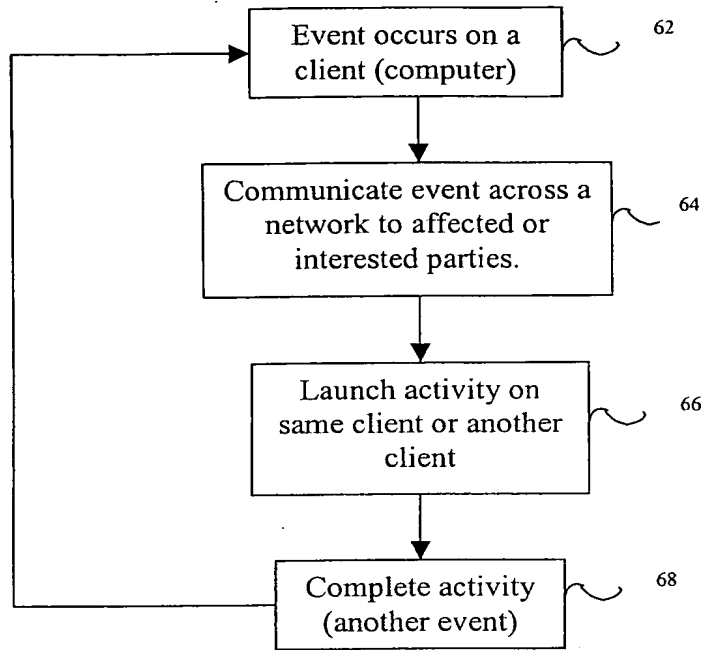


FIG. 6

Top Level Process: A

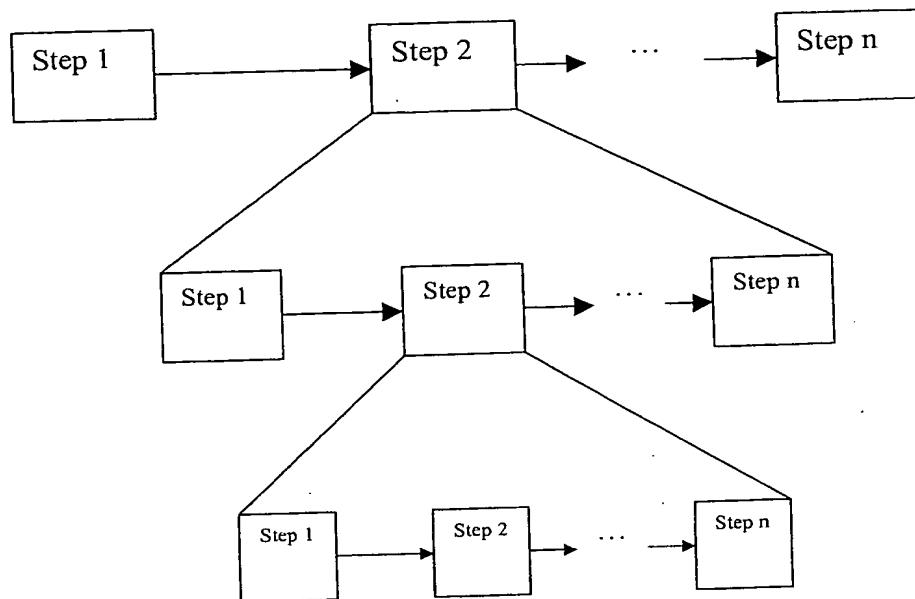


FIG. 8

FIG. 8

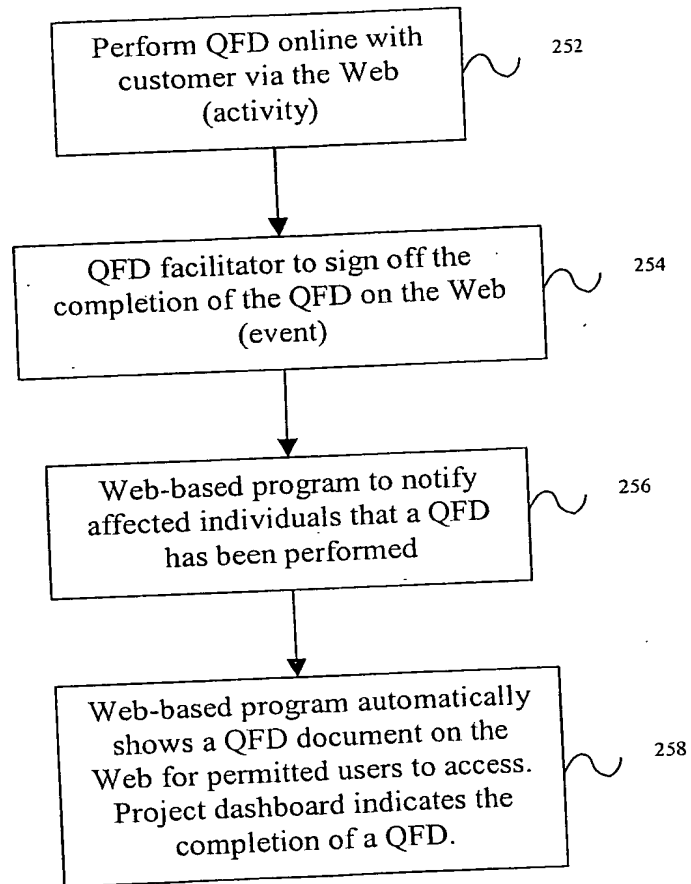


FIG. 9

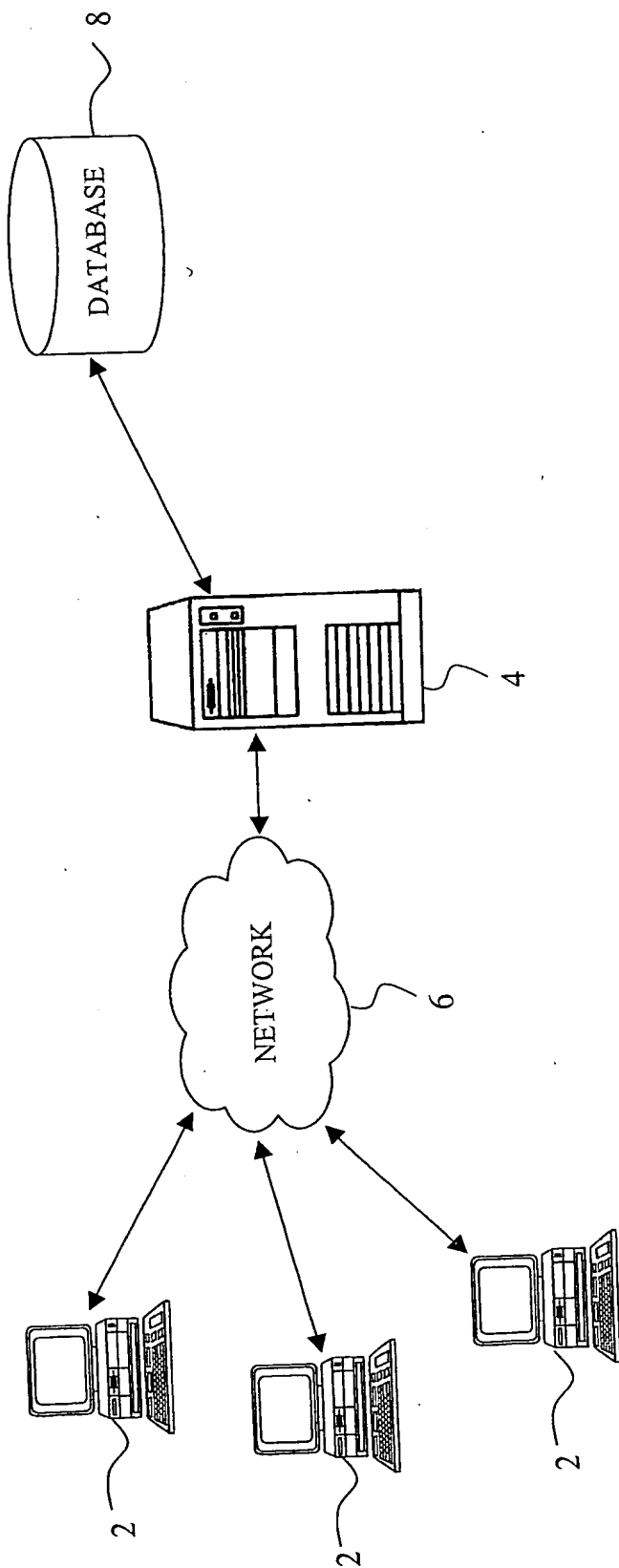
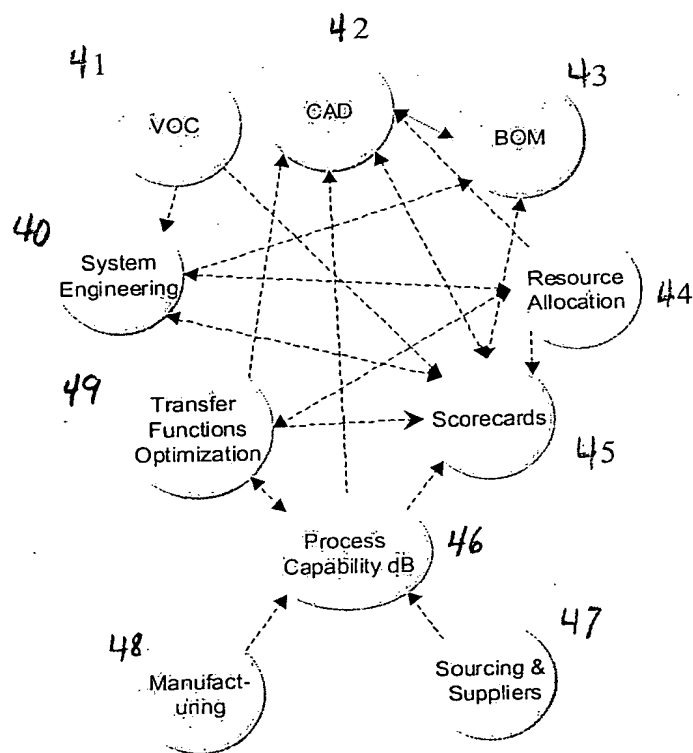


FIG. 10

FIG. 11



PRIOR ART

FIG. 11

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FIG. 12

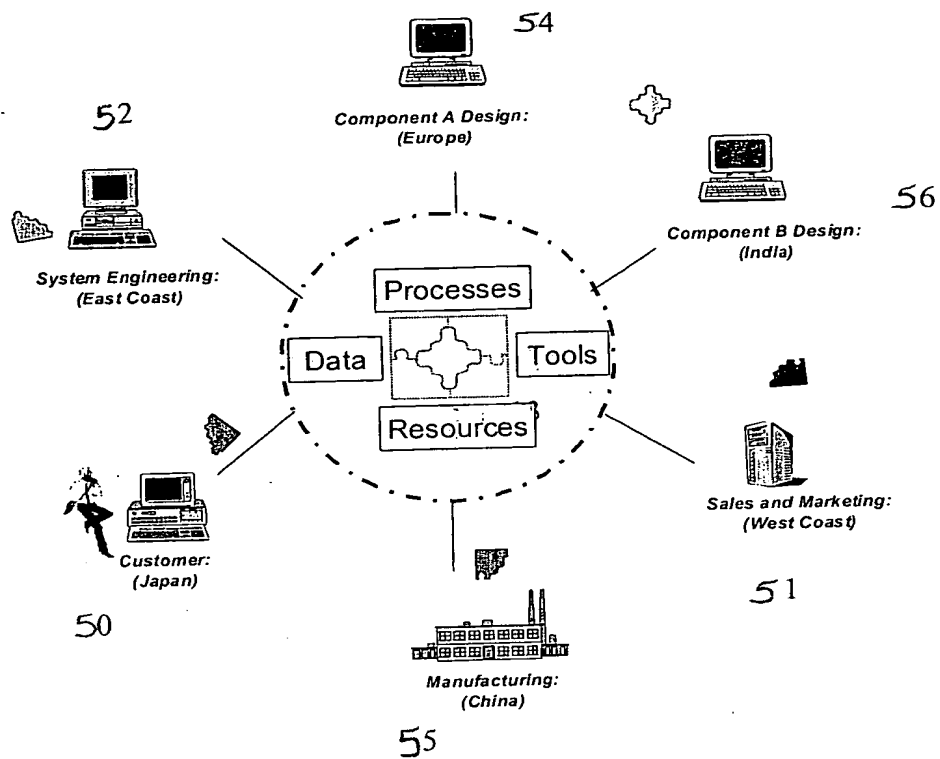


FIG. 12

FIG. 13

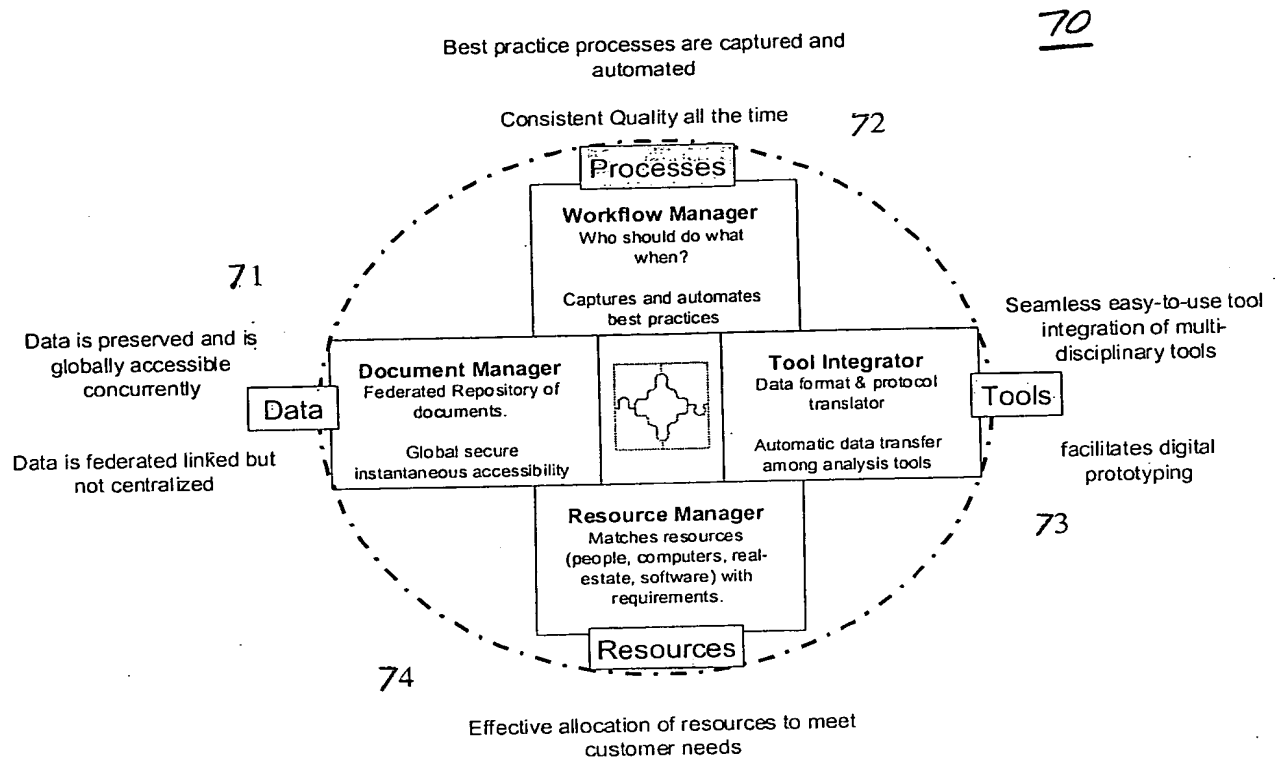


FIG. 13

FIG. 14

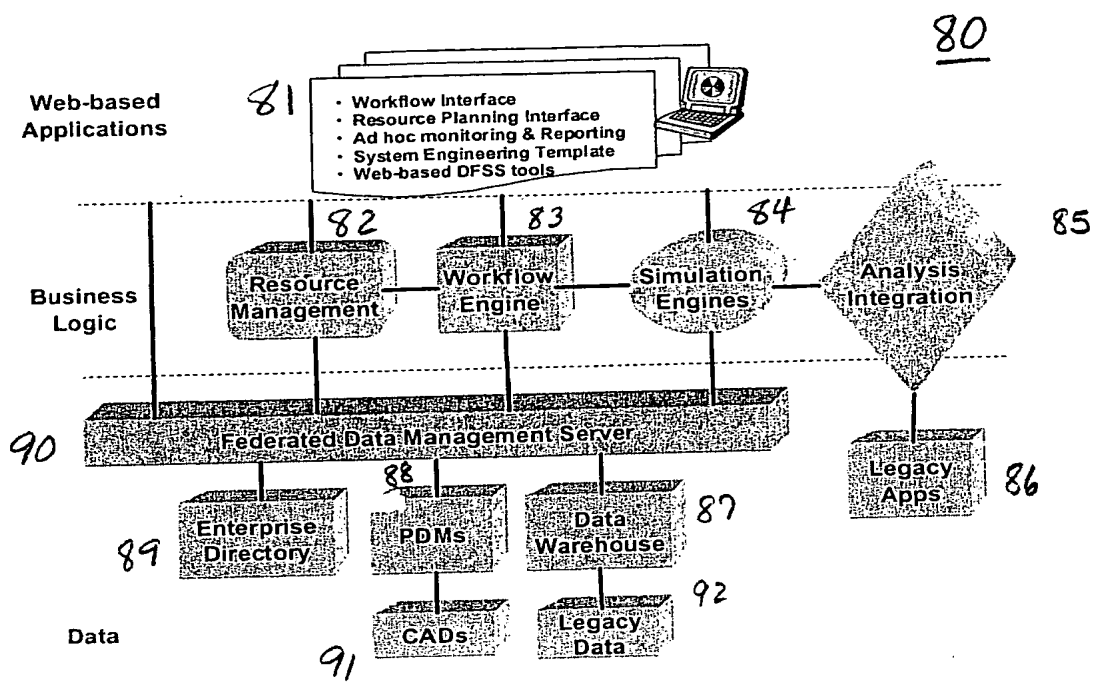


FIG. 14

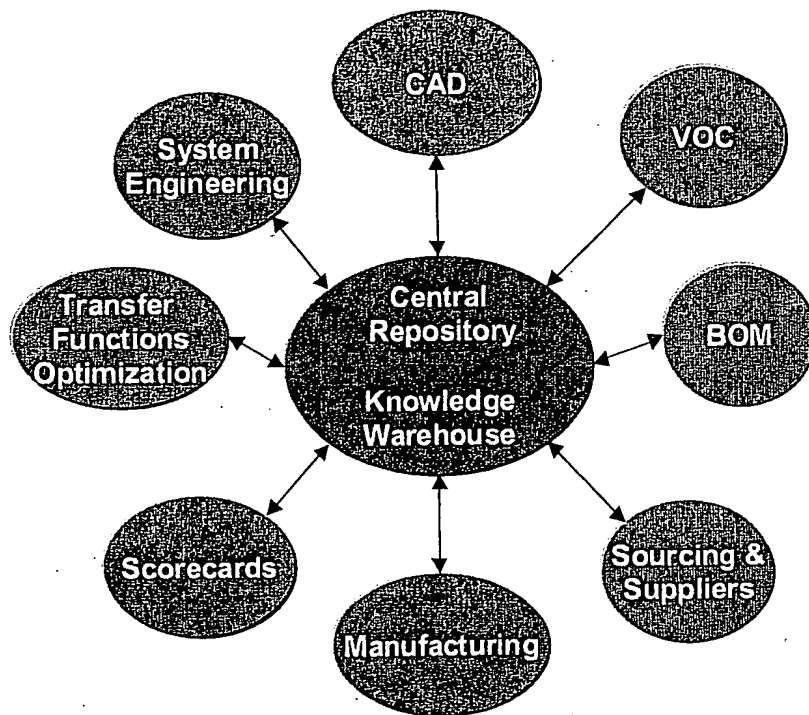


FIG. 15